# **ASi Slave Evaluation Board**



THE AS-INTERFACE MASTERS

E

### **ASi Slave Evaluation Board**

Article no. BW1423 on basis ASI-SW+





## Article no. BW1423

The ASi Slave Evaluation Board on basis of the ASI-SW+ chip by ZMD can be connected to the ASi circuit directly and without any additional hardware. It is a complete AS-i slave. Voltage supply is provided by the ASi line.

The ASi Slave Evaluation Board is suitable for **experiments** and **test setups** and fully able to function without additional hardware. Linked to an ASi network, it enables the ASi Master to set parameter outputs (PWM function) and data outputs as well as to read data inputs.

Connections:		
ASI+, ASI-	ASi connection	
Uout	DC output voltage max. 20 mA	
DI2 to DI3	Data inputs	
DO0 to DO1	Data outputs	
FID	Periphery fault (0: no fault)	
GND	Ground	

The slave board can be simply stuck upon a bigger prototyping board (piggy back), or via screwed terminals be attached directly to the ASi cable.

The ID codes and the IO code can be programmed with the help of the A<sup>2</sup>SI/SAP4 Programming and Test Tool (Article-no. BW1355) and the ASi 2.1 Function and EMC-Test Masters (Article no. BW1259, BW1260)



#### **Technical data**

Operating current	Aprox. 20 mA (with external user application)
Operating voltage	ASi voltage 30 V DC
Function display	Power-on LED: green
	Error: red
Dimensions (L, W, H)	28 mm, 31 mm, 8 mm

#### Programming:

**Default setting** 

IO Code B ID Code A ID2 Code E

The ID codes and the IO code can be programmed with the help of the A<sup>2</sup>SI/SAP4 Programming and Test Tool (Article no. BW1355, see page 79) and the ASi 2.1 Function and EMC-Test Masters (Article no. BW1259, BW1260).

The ID codes and IO codes for the different types of slaves have to be asked for at the ASinternational Association. The data sheet of the ASI-SW+ chip:

http://www.zmd.de/as\_interface.html